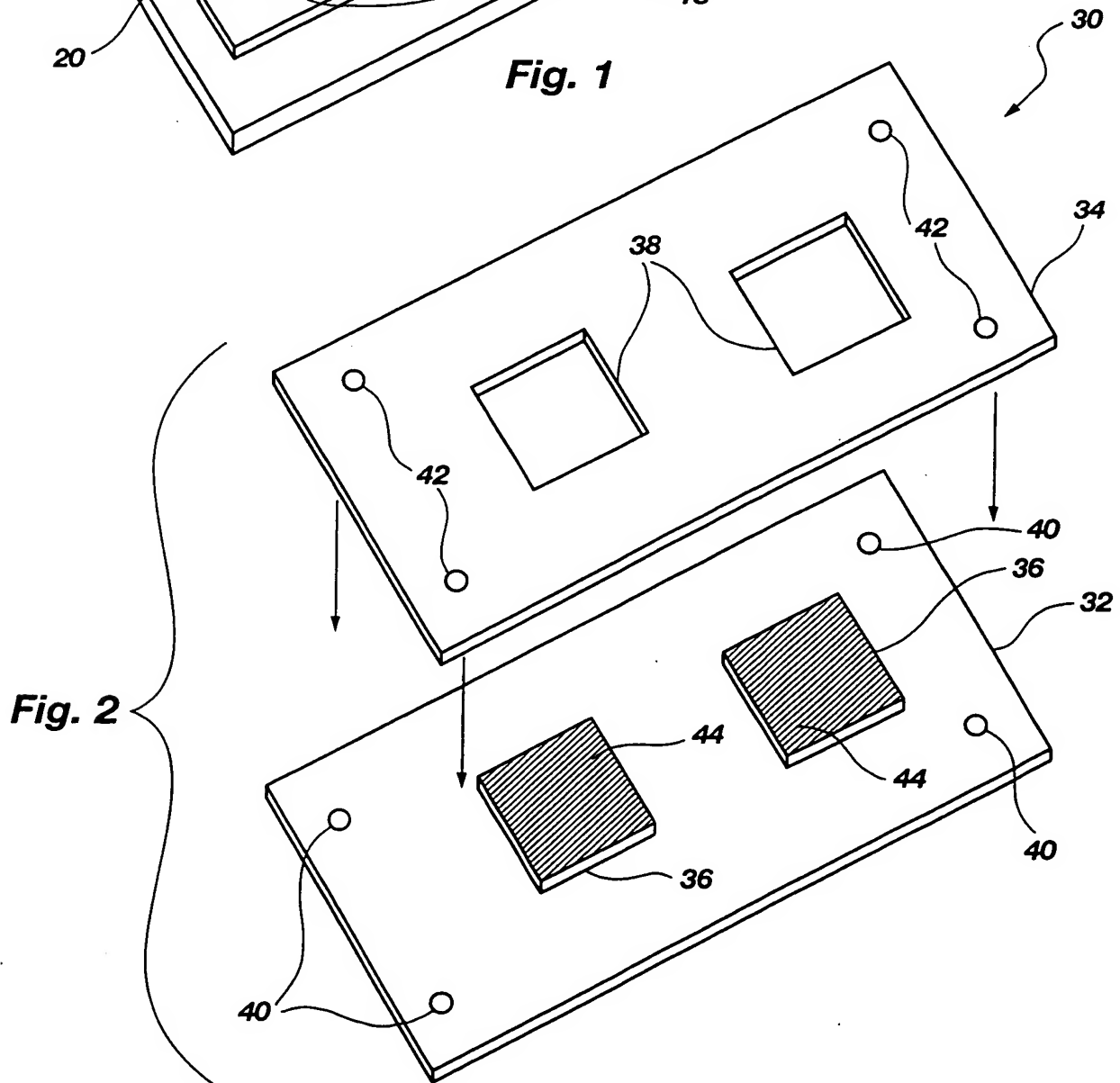
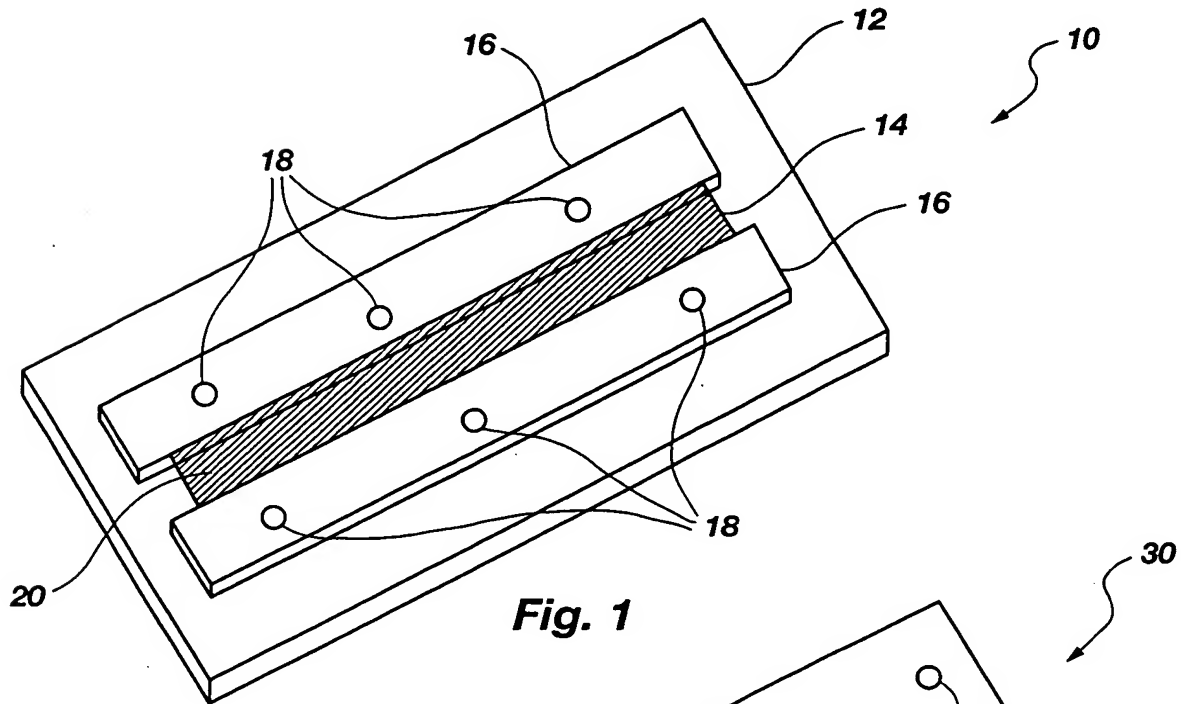


1/3



2/3

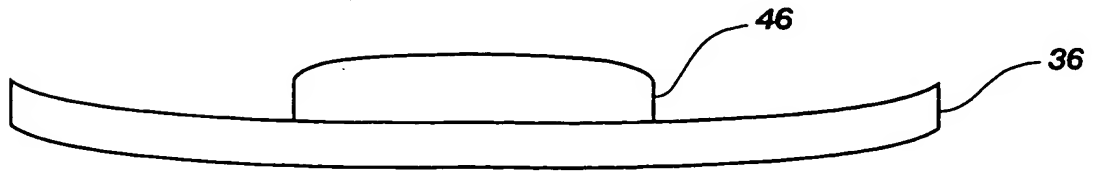


Fig. 3

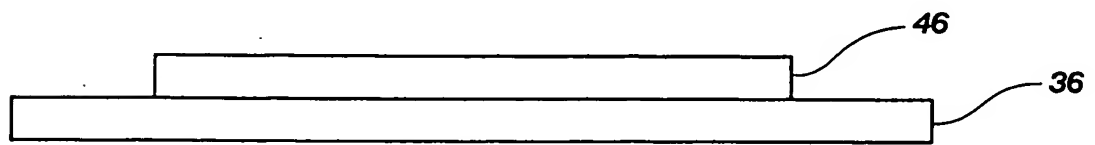


Fig. 4

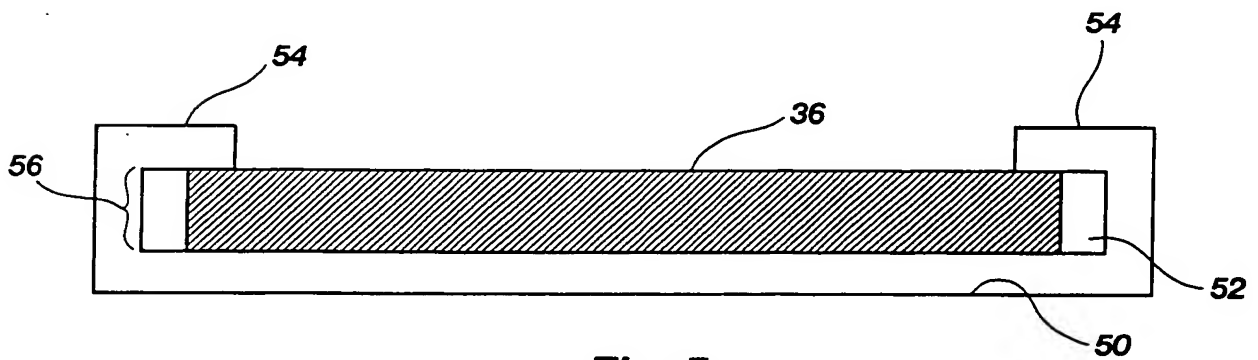


Fig. 5

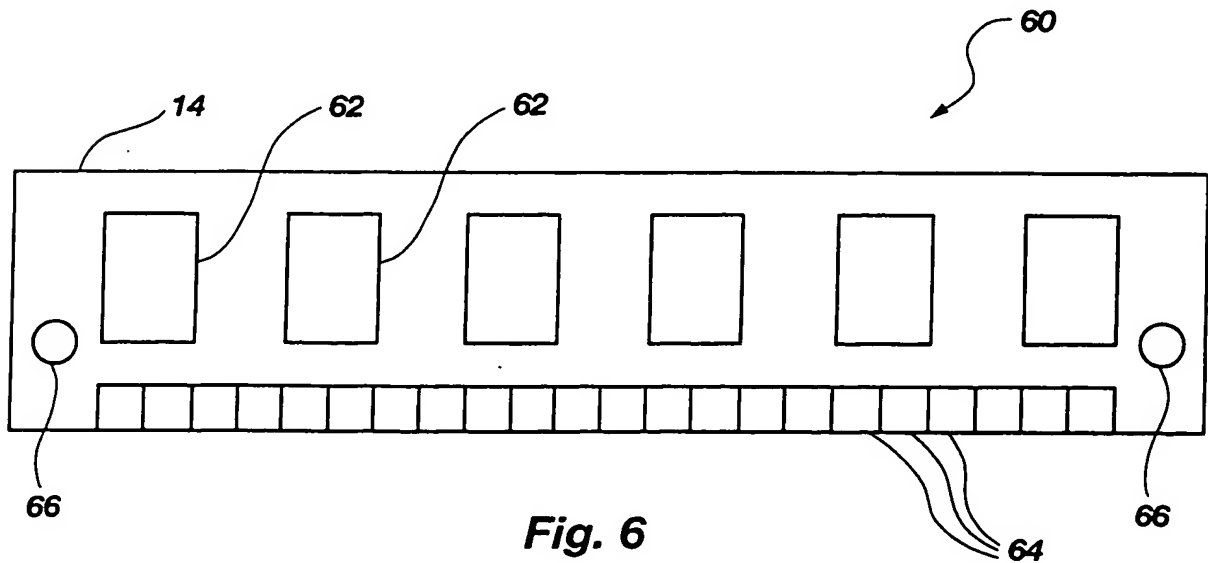


Fig. 6

TITLE: METHOD AND APPARATUS FOR REDUCING WARPAGE
DURING APPLICATION AND CURING OF ENCAPSULANT
MATERIALS ON A PRINTED CIRCUIT BOARD

Inventor: Derek J. Gochnour
Docket No.: 3574.4US

3/3

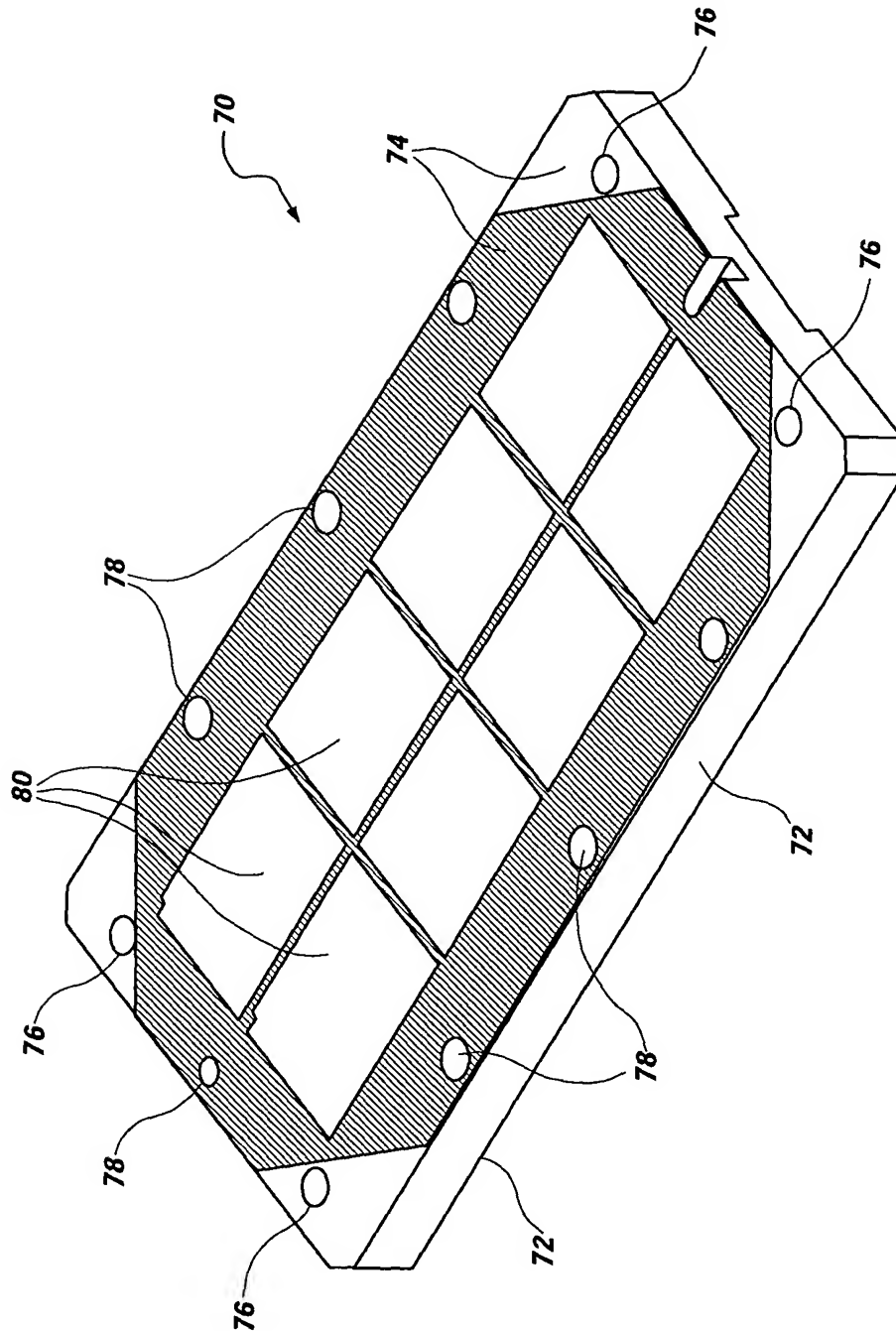


Fig. 7